

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF: U.S. Patent No. 7,391,840

USPTO CONFIRMATION CODE: 5687

APPLICATION NO.: 10/517,181

PCT FILED: July 03, 2002

U.S. FILED: December 07, 2004

EXAMINER: Marsha D. Banks-Harold

GROUP ART UNIT: 2621

FOR: PHASE LOCKED LOOP CIRCUIT, ELECTRONIC DEVICE INCLUDING A  
PHASE LOCKED LOOP CIRCUIT AND METHOD FOR GENERATING A  
PERIODIC SIGNAL

37 CFR 1.322 & 37 CFR 1.323 REQUEST FOR CERTIFICATE OF CORRECTION  
FOR USPTO AND/OR APPLICANT MISTAKE

HONORABLE COMMISSIONER OF PATENTS & TRADEMARKS

SIR:

The following is a request for a certificate of correction in Serial Number 10/517,181, now Patent Number 7,391,840.

A certificate of correction under 35 USC 254 is respectfully requested in the above-identified patent.

All errors were the fault of the USPTO, no fee required. In the event that a further fee is required, please charge the amount to our Deposit Account No. 50-1379.

The exact locations where the errors appear in the patent and patent application are as follows:

$$H_{closed} = \frac{K_D H_f(s) \frac{K_o}{s}}{1 + \left[ \frac{1 + \tau_s s}{N} \right] K_D H_f(s) \frac{K_o}{s}}$$

In Column 4, Line 16, in Equation (4), delete “

$$H_{closed} = \frac{K_D H_f(s) \frac{K_o}{s}}{1 + \left[ \frac{1 + \tau_s s}{N} \right] K_D H_f(s) \frac{K_o}{s}}$$

and insert --

--, therefor.

(ORIGINALLY FILED SPECIFICATION DATED DECEMBER 7, 2004, PAGE 6, LINE 5)

The requested corrections are attached on Form PTO 1050.

Respectfully Submitted

DEC 1, 2008

DATE

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